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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/703,034	10/31/2000	Joseph R. Zbiciak	TI-30553	8913
23494 75	590 11/03/2006		EXAMINER	
TEXAS INST	RUMENTS INCORPOR	DO, CHAT C		
P O BOX 6554	74, M/S 3999			
DALLAS, TX 75265			ART UNIT	PAPER NUMBER
			2193	
			DATE MAILED: 11/03/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

## Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	Applicant(s)	
09/703,034	ZBICIAK, JOSEPH R.		
Examiner	Art Unit		
Chat C. Do	2193		

TI MANUAL DATE CALL
The MAILING DATE of this communication appears on the cover sheet with the correspondence address
THE REPLY FILED 10 October 2006 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.
1.  The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:
a) The period for reply expiresmonths from the mailing date of the final rejection.
b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN
TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).
Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).  NOTICE OF APPEAL
2. The Notice of Appeal was filed on A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of
filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).  AMENDMENTS
3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will <u>not</u> be entered because  (a) They raise new issues that would require further consideration and/or search (see NOTE below);  (b) They raise the issue of new matter (see NOTE below);
(c) They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
(d) They present additional claims without canceling a corresponding number of finally rejected claims.  NOTE: (See 37 CFR 1.116 and 41.33(a)).
4. The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
5. Applicant's reply has overcome the following rejection(s):
6. Newly proposed or amended claim(s) would be allowable if submitted in a separate, timely filed amendment canceling the
non-allowable claim(s).  7. For purposes of appeal, the proposed amendment(s): a) will not be entered, or b) will be entered and an explanation of
how the new or amended claims would be rejected is provided below or appended.  The status of the claim(s) is (or will be) as follows:
Claim(s) allowed:
Claim(s) objected to: Claim(s) rejected: <u>1, 4-5, 10-11, 13</u> .
Claim(s) withdrawn from consideration:
AFFIDAVIT OR OTHER EVIDENCE
8. The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will <u>not</u> be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
9. The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will <u>not</u> be entered because the affidavit or other evidence failed to overcome <u>all</u> rejections under appeal and/or appellant fails to provide a showing a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
10. The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.
REQUEST FOR RECONSIDERATION/OTHER
11. The request for reconsideration has been considered but does NOT place the application in condition for allowance because: <u>See below.</u>
12. Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s).
13. Other:
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Part 11: The applicant argues repeatedly in pages 5-6 for independent claims 1 and 13 that the cited reference by Saishi et al. fails to disclose the carry input to a mid-position receiving said rounding value to form the intermediate result.

Saishi et al. clearly define or disclose the "the carry input to a mid-position receiving said rounding value to a form the intermediate result" in Figure 8 wherein k is set to zero. If the rounding position is (m+k)th bit and k is equate to zero, then the rounding position is mth bit out of 2m bit product which is a mid-position of the product 803. After adding 1 as rounding factor into the mid-position in step 806, the final rounding product can be obtain by either chopping the lower part (e.g. obtaining only the first half portion of rounding) or shifting the final rounding product by m-bit to obtain the final result. Generally, Figure 8 clearly discloses the rounding factor is inserted or added into the intermediated multiplication result prior shifting and perform shifting to select the desired portion for the final multiplication product.

The applicant also argues in page 7 that the cited references fail to disclose the rounding takes palce "via an arithmetic circuit". The examiner respectfully submits that the claim or the limitation "via an arithmetic circuit" does not define or limit the structure of the circuit. The claims 1 and 13 do not disclose what should be included or excluded in the via an arithmetic circuit. Thus, any circuit comprising any means for performing the rounding after combining the products would meet the claim limitation.

Finally, the applicant argues in page 8 for claim 10 that the cited reference fails to disclose the limitation cited in claim 10. The examiner respectfully submits that the limitation cited in claim 10 is a well known limitation which is widely used in dot product. The limitation "subtracting the product of the second pair of elements from the product of the first pair of elements" is not a gist of invention.

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